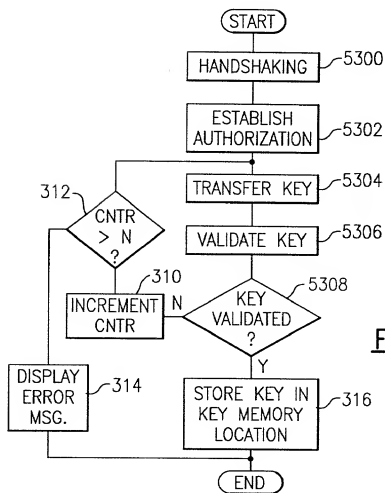
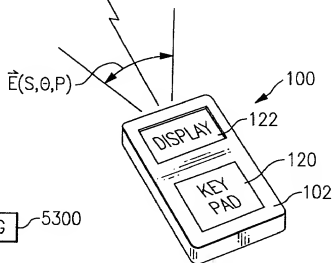
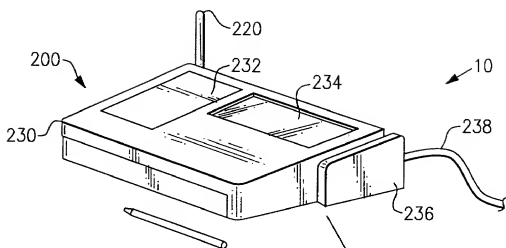
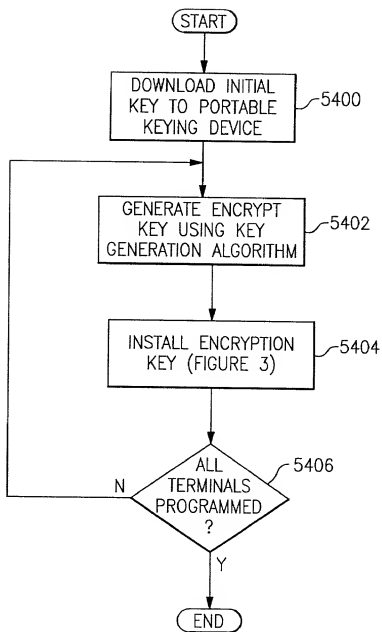
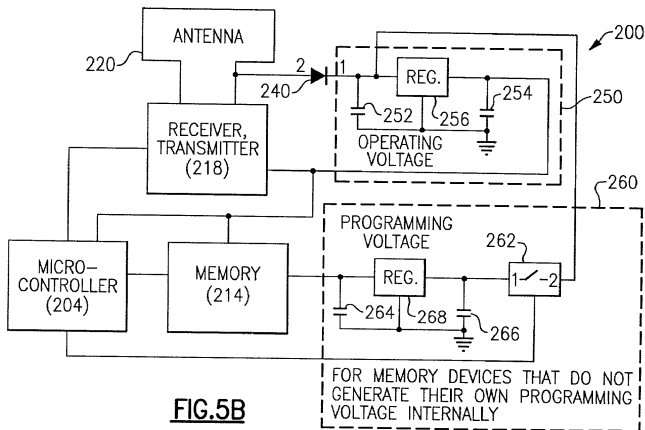
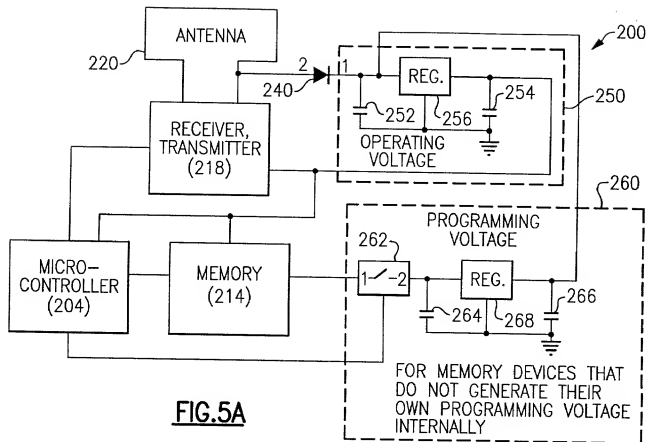


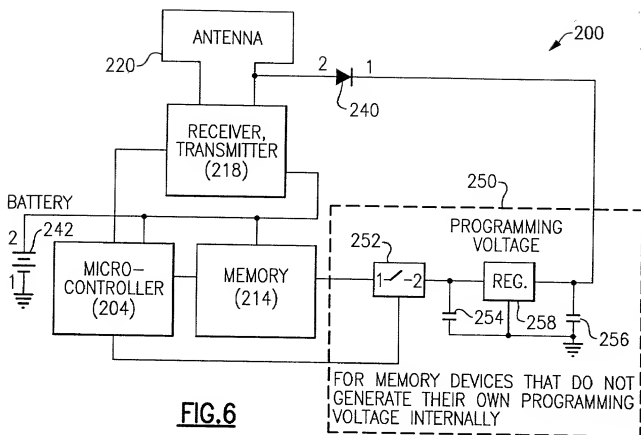
**FIG.1**





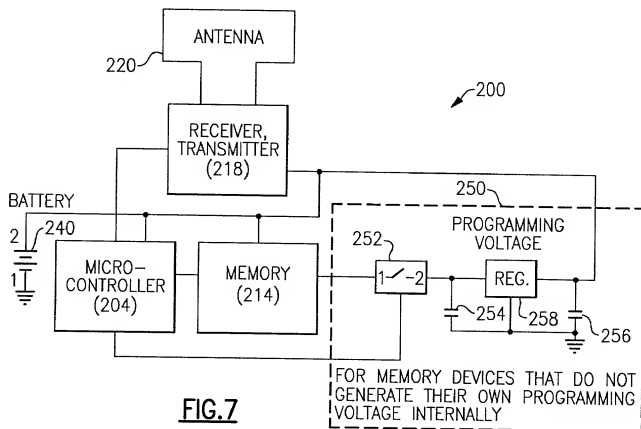
**FIG.4**





**FIG.6**

FOR MEMORY DEVICES THAT DO NOT  
GENERATE THEIR OWN PROGRAMMING  
VOLTAGE INTERNALLY



**FIG.7**

FOR MEMORY DEVICES THAT DO NOT  
GENERATE THEIR OWN PROGRAMMING  
VOLTAGE INTERNALLY